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| <b>Search Notes</b><br><br> | <b>Application/Control No.</b><br><br>10788455 | <b>Applicant(s)/Patent Under Reexamination</b><br><br>BODELL ET AL. |
|  | <b>Examiner</b><br><br>MUKTESH G GUPTA         | <b>Art Unit</b><br><br>4121   |

| SEARCHED |          |            |          |
|----------|----------|------------|----------|
| Class    | Subclass | Date       | Examiner |
| 709      | 249      | 10/31/2007 | MG       |

| SEARCH NOTES   |            |          |
|--|------------|----------|
| Search Notes   | Date       | Examiner |
| Inventor Name search in Palm and East  | 12/06/2007 | MG       |
| (supercomputers or HPC or multiprocessor or nodes or communication or network) same (data or exchange or transfer or flow or bandwidth or latency or packet or protocol or address or bus or FSB or SMP or interface or interconnects) | 12/06/2007 | MG       |
| (CRC or Full-duplex or bi-directional or tree or topology or I-O or EMI or parallel or serial or lines or skew or cross-talk or reduce or package or NIC)  | 12/06/2007 | MG       |
| NPL document searched on Internet  | 12/07/2007 | MG       |

| INTERFERENCE SEARCH |          |      |          |
|---------------------|----------|------|----------|
| Class               | Subclass | Date | Examiner |
|                     |          |      |          |